

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

PPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/064,673	08/06/2002	Ming Wen	IACP0009USA	5331
27765 75	590 11/17/2004		EXAMINER	
NAIPO (NORTH AMERICA INTERNATIONAL PATENT OFFICE)			NGUYEN, DONGHAI D	
P.O. BOX 506 MERRIFIELD, VA 22116		ART UNIT	PAPER NUMBER	
			3729	
			DATE MAILED: 11/17/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

·						
	Application No.	Applicant(s)				
	10/064,673	WEN ET AL.				
Office Action Summary	Examiner	Art Unit				
	Donghai D. Nguyen	3729				
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the o	correspondence address				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply fix to reply is specified above, the maximum statutory period. - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	.136(a). In no event, however, may a reply be tin only within the statutory minimum of thirty (30) day I will apply and will expire SIX (6) MONTHS from te. cause the application to become ABANDONE	mely filed ys will be considered timely. In the mailing date of this communication. ED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 06 A	August 2002.					
2a) ☐ This action is FINAL. 2b) ☑ Thi	is action is non-final.					
•—	— ···					
closed in accordance with the practice under	Ex parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.				
Disposition of Claims						
4) Claim(s) 1-8 is/are pending in the application.	•					
4a) Of the above claim(s) is/are withdra	awn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-8</u> is/are rejected.						
7) Claim(s) is/are objected to.	or alaction requirement					
8) Claim(s) are subject to restriction and/	or election requirement.					
Application Papers						
9)☐ The specification is objected to by the Examin						
10) ☐ The drawing(s) filed on is/are: a) ☐ ac						
Applicant may not request that any objection to the	- · ·					
Replacement drawing sheet(s) including the corre						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreig	n priority under 35 U.S.C. § 119(a	a)-(d) or (f).				
a) ☐ All b) ☐ Some * c) ☐ None of: 1.☐ Certified copies of the priority documer	nts have been received					
2. Certified copies of the priority documer		tion No.				
3. Copies of the certified copies of the pri						
application from the International Bure						
* See the attached detailed Office action for a lis	st of the certified copies not receiv	ed.				
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) Interview Summar					
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/06 Paper No(s)/Mail Date 	Paper No(s)/Mail D 5) Notice of Informal 6) Other:	Pate Patent Application (PTO-152)				
S. Patent and Trademark Office						

Page 2

Application/Control Number: 10/064,673

Art Unit: 3729

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1-4 are rejected under 35 U.S.C. 102(e) as being anticipated by US Patent No. 6,366,472 to Alina et al.

Regarding claims 1-2, Alina et al disclose a method of fastening an electromagnetic interference (EMI) shield (30/51) to a printed circuit board (34/54), the printed circuit board comprising a plurality of holes (slot), the EMI shield comprising a plurality of fasteners (56) corresponding to the plurality of holes of the printed circuit board, the method comprising: inserting the plurality of fasteners into the plurality of holes (slot Col. 2, lines 20-23) to securely fasten the EMI shield to the printed circuit board (Figs. 3/5).

Regarding claims 3-4, Figs. 3 and 5 show a height of the plurality of fasteners is no greater than a thickness of the circuit board and plurality of fasteners is joined to the plurality of holes by intrusion.

3. Claims 1-2 and 5-8 are rejected under 35 U.S.C. 102(b) as being anticipated by US Patent No. 6.239,359 to Lilienthal, II et al.

Application/Control Number: 10/064,673

Art Unit: 3729

Regarding claims 1-2, Lilienthal, II et al disclose a method of fastening an electromagnetic interference (EMI) shield (22) to a printed circuit board (20), the printed circuit board comprising a plurality of holes (opening 72), the EMI shield comprising a plurality of fasteners (44/50/58/80) corresponding to the plurality of holes of the printed circuit board, the method comprising: inserting the plurality of fasteners into the plurality of holes (72) to securely fasten the EMI shield to the printed circuit board (Figs. 3/4/8/etc.).

Regarding claims 5 and 6, Figs. 10 or 12 shows the EMI shield further comprises a plurality of fixed fittings (58/80) used to fix the EMI shield to another structure and the plurality of fixed fittings is of a pushbutton structure (60/92).

Regarding claims 7 and 8, Figs. 6, 7 or 14 shows a height of the plurality of fasteners is greater than a thickness of the circuit board and after the plurality of fasteners is inserted into the plurality of holes, ends of the plurality of fasteners are bent under the circuit board (Notes that the prongs 46 or 98 are bent back to it normal sized after being bent to fit through the holes).

Conclusion

- 4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- 5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Donghai D. Nguyen whose telephone number is (703) 305-7859. The examiner can normally be reached on Monday-Friday (9:00-6:00).

Application/Control Number: 10/064,673

Art Unit: 3729

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Peter D. Vo can be reached on (703) 308-1789. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DN

PETER VO
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 3700

Page 4